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** CONTINUING DATA *****

This application is a DIV of 09/677,743 10/03/2000 PAT 6,611,060

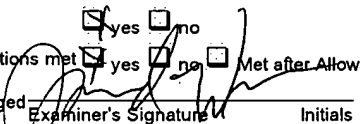
** FOREIGN APPLICATIONS *****

JAPAN 11-282404 10/04/1999

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged  Examiner's Signature Initials			

ADDRESS

22852

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TITLE

Method of manufacturing semiconductor device and semiconductor device

including Two-step Polishing operation for
 Cap metal

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